

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,031,689 B2  
APPLICATION NO. : 10/008492  
DATED : April 18, 2006  
INVENTOR(S) : Michael Frank

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 4 Line 13 In Claim 1, delete "tow" and insert -- two --, therefor.

Col. 4 Line 21 In Claim 1, before "a" insert -- a first field effect transistor (FET), having a gate connected to the first IMN output; --, as a separate line therefor.

Col. 4 Line 25 In Claim 1, delete "FET;" and insert -- FET [at a first node]; --, therefor.

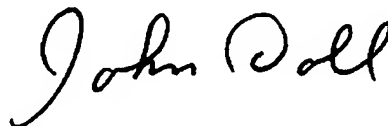
Col. 4 Line 37(Approx.) In Claim 1, before "differential" insert -- first --.

Col. 4 Line 42(Approx.) In Claim 1, delete "signal," and insert -- signal --, therefor.

Col. 4 Line 42 In Claim 1, after "section," insert -- conditioning circuit, --.

Signed and Sealed this

Seventeenth Day of February, 2009



JOHN DOLL  
*Acting Director of the United States Patent and Trademark Office*